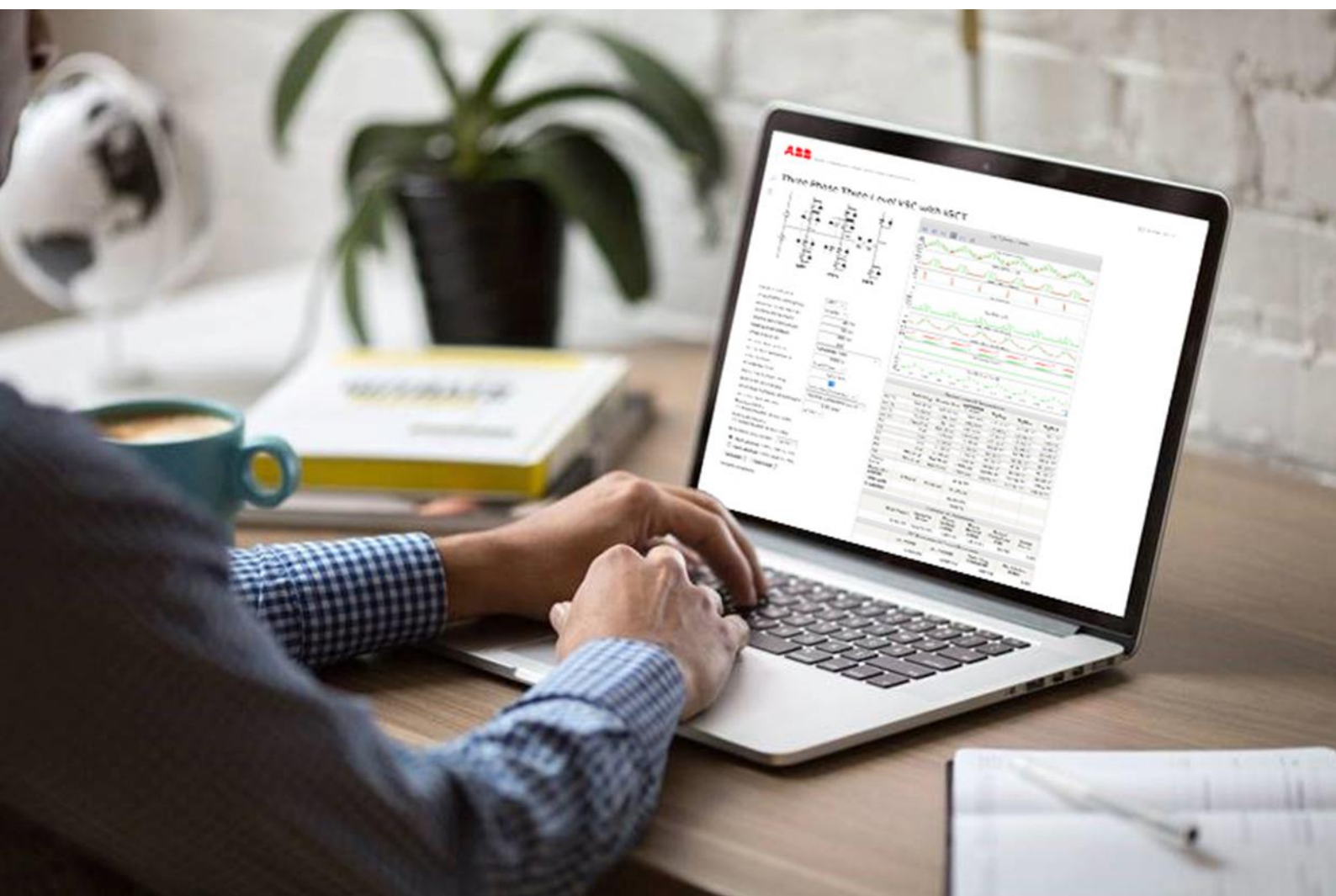


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# SEMIS Simulation Tool

## Three Phase 3-level VSC with IGCT

### User manual





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## INTRODUCTION

SEMIS is a web-based semiconductor simulation tool providing a thermal calculation of the semiconductor losses for common converter circuits. The simulation simplifies significantly the selection of the switching device and enables the optimal selection of semiconductors for further investigations.

The SEMIS Simulation Tool is a user-friendly online application found on ABB Semiconductors website [www.abb.com/semiconductors/semis](http://www.abb.com/semiconductors/semis)

SEMIS users select from a substantial selection of topologies. By assigning the circuit parameters and selecting the desired switching device, multiple ABB products can be simulated at the same time. Once a simulation is run, SEMIS returns comprehensive results on semiconductor losses as well as on the electrical parameters in the input and output of the circuit. The results are shown in both graphical (waveforms) and numerical (tables) way.

The SEMIS tool is based on the PLECS simulation software. PLECS users can download our product models in the XML file format from the ABB Semiconductors website and use them for their own simulations. For more specific topologies ABB offers customized converter simulations for non-standard topologies with PLECS simulation software on a project basis.

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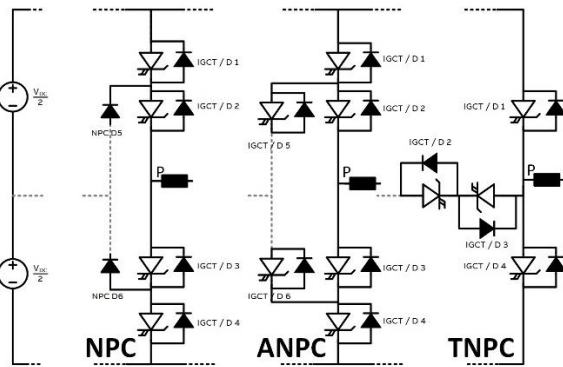
### 1. 3 PHASE 3 LEVEL VSC CONVERTER

The use of powerful modular three-phase 3-level VSC converters are very popular and are used in various grid-tied applications for DC-AC (Three-phase Inverter) and AC-DC (Three-phase Rectifier) operation. Due to the simplicity of the architecture and control architecture the three-phase 3-level VSC are widely used in various products.

ABB offers the following Three-phase topologies for thermal analysis simulation with

- Three-Phase Two-level VSC with IGBT
- Three-Phase Three-level VSC with IGBT (NPC, TNPC, ANPC)
- Three-Phase Three-level VSC with IGCT (NPC, TNPC, ANPC)
- Three-Phase Three-level VSC with IGBT Half-Bridge MMC
- Three-Phase Three-level VSC with IGCT Half-Bridge MMC
- Three-Phase Three-level VSC with Full Bridge MMC
- Three-Phase Three-level VSC FACTS with IGBT Full Bridge
- Three-Phase Three-level VSC FACTS with IGCT Full Bridge

## 2. OVERVIEW



Select Toplogy:

Converter Operation:

Ambient temperature:  °C

System Frequency:  Hz

Switching frequency:  Hz

PWM strategy:

Modulation Index:

DC Voltage:  V

---

AC Output Reference:

Load Power:  kVA

Power Factor:

Reactive Power Type:

---

IGCT Type Selection:

IGCT Heat Sink Thermal Resistance:  K/W

Diode Heat Sink Thermal Resistance:  K/W

IGCT Voltage Rating:  kV

Matching IGCTs:

- 5SHY 35L4520 1700 A, 125C
- 5SHY 35L4521 1700 A, 125C
- 5SHY 35L4522 2100 A, 125C
- 5SHY 40L4511 1430 A, 125C
- 5SHY 55L4500 1870 A, 125C

AntiParallel Diodes:

- 5SDF 20L4520 4.5kV, 1950 A, 140C
- 5SDF 28L4520 4.5kV, 2620 A, 140C

NPC Diode Selection:  kV

- 5SDF 20L4520 4.5kV, 1950 A, 140C
- 5SDF 28L4520 4.5kV, 2620 A, 140C

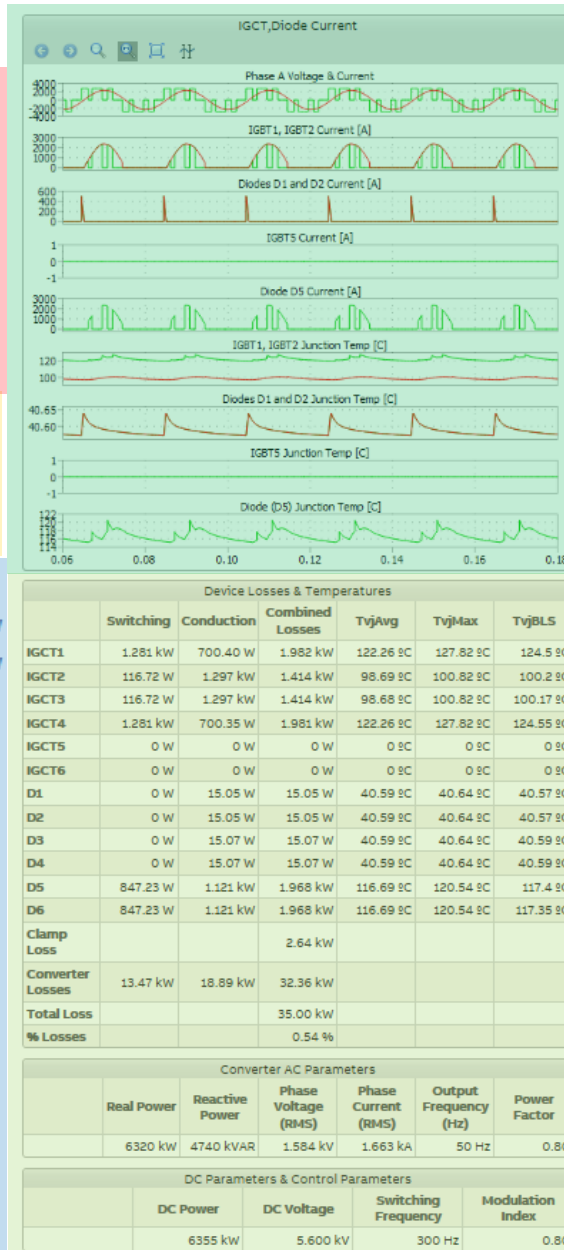


Figure 1 Page layout of 3-Phase NPC 3-Level converter

- Grid definitions
- Converter settings
- IGCT selection

- Results graphs
- Results tables

Remark: For NPC topology the selection of diode is enabled

## 2.1 Three-phase 3-level VSC power circuit schematic

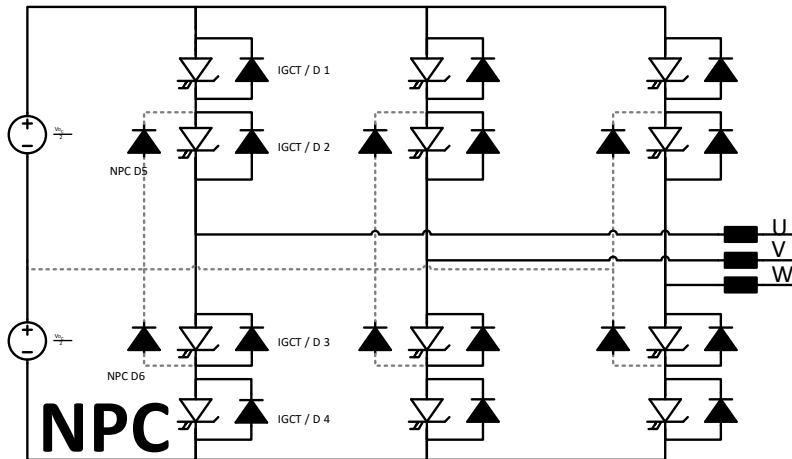


Figure 2 Three-phase 3-level NPC VSC power circuit schematic

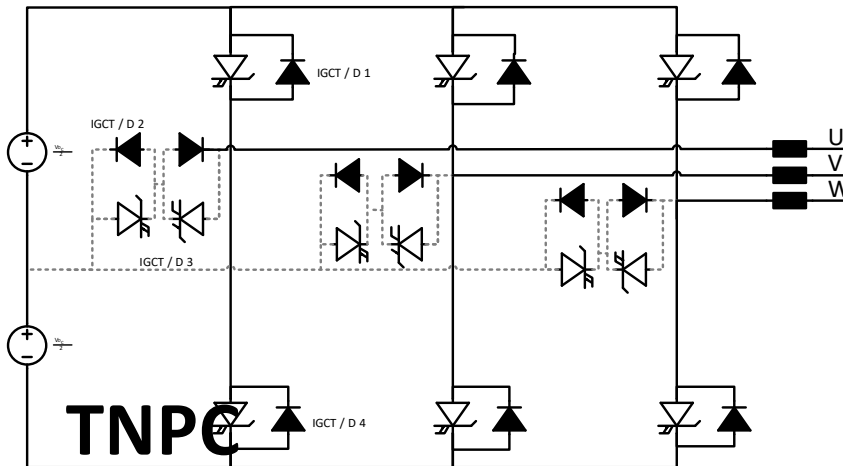


Figure 3 Three-phase 3-level TNPC VSC power circuit schematic

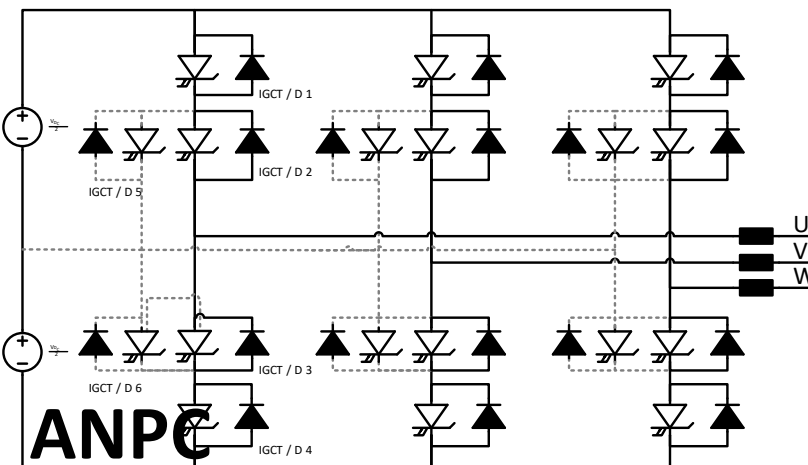


Figure 4 Three-phase 3-level ANPC VSC power circuit schematic



3. SIMULATION SETTINGS

3.1 Circuit parameters

3.1.1 Converter Operation

Select Topology		Selection
	Topology can be selected either as NPC or as TNPC or as ANPC.	
Converter Operation		Selection
	Converter can be operated either as Inverter DC to AC or as Rectifier AC to DC	

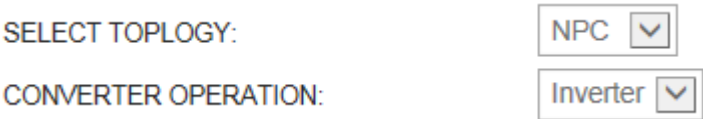


Figure 5 Converter topology and mode selection

3.1.2 Ambient temperature

Ambient temperature	Definition of environmental temperature around the converter for temperature / cooling calculations	Range -25 .. 90 °C
---------------------	---	--------------------



Figure 6 Ambient temperature input block

3.1.3 Controller

The user can define the following parameters as seen in figure 4 . The controller generates the switching pulses for the upper and lower IGCTs of the converter.

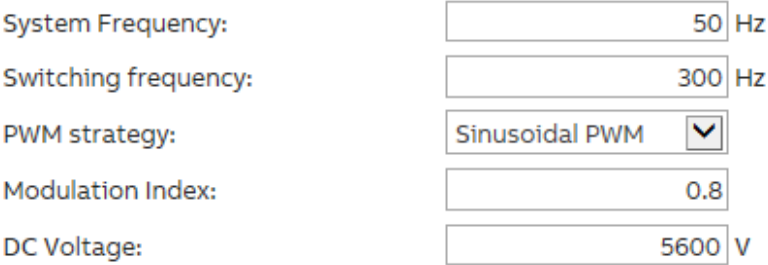


Figure 7 Controller input block

FREQUENCY	Converter AC output frequency	Range 1 to 500 Hz
SWITCHING FREQUENCY	Definition of switching frequency applied for PWM control (Phase-shifted PWM)	Range 200 to 10000 Hz
PWM Strategy	Definition of PWM strategy Different Control methods are implemented, which are SPWM, SVPWM, THIPWM and DPWMs	Selection

## Simulation Settings

ANPC topology has again 3 different controls. Find technical background and explanations in Chapter 5.

MODULATION INDEX	Definition of modulation index Sinusoidal PWM limit is 1.00 Rest of PWMs limit is 1.15	Range 0 .. 1 (1.15)
DC Voltage	Converter DC Pole-Pole Voltage	Range 100 to 9000 V

### 3.1.4 Load parameters

The user can enter the desired reference converter AC side current (RMS) or AC power. Further, the user can provide the AC parameters such as power factor and the nature of reactive power to be supplied (Inductive or Capacitive).

AC OUTPUT REFERENCE:

LOAD POWER:  kVA

POWER FACTOR:

REACTIVE POWER TYPE:

**Figure 8 Grid/Load parameter input blocks**

AC REFERENCE PARAMETERS	Load Reference can be selected as AC Power when AC Power is the reference AC Current when AC Current (RMS) is the reference	Selection
AC SIDE POWER	AC Side Power demand from the load / connected grid	Range 1 .. 10000 kVA
AC SIDE CURRENT(RMS)	AC Side Current demand from the load/ connected grid	Range 1 .. 5000 A
POWER FACTOR VALUE	Power Factor of the load/ connected grid	Range 0 .. 1
POWER FACTOR TYPE	The power factor type can be selected as Inductive or Capacitive based on lagging or leading power factor	Selection

### 3.2 Switch settings

IGCT TYPE SELECTION:

IGCT HEAT SINK THERMAL RESISTANCE:  K/W

DIODE HEAT SINK THERMAL RESISTANCE:  K/W

IGCT VOLTAGE RATING:

**Figure 9 Thermal settings and IGCT selection**

IGCT Type selection	Select type of IGCT either Asymmetric or Reverse Conducting for filtering	Selection
Heat Sink Thermal Resistance	Definition of thermal resistance of cooling system applied.	Range 0.0001 .. 0.5 K/W

**Remark:** The value entered is attributed to each individual switch shown in the electrical configuration schematic of the IGCT module data sheet.

The selected Rth is also accounted for the antiparallel diode position for which same consideration applies for its electrical configuration.

IGCT selection                      Select voltage class of IGCT for filtering                      Selection

### 3.2.1 Matching IGCTs

Once the previous IGCT properties are selected, the matching IGCT options appear. By clicking on the product code name the user may access the data sheet from the ABB website.

Matching IGCTs:

- [5SHY 35L4520](#) 1700 A, 125C
- [5SHY 35L4521](#) 1700 A, 125C
- [5SHY 35L4522](#) 2100 A, 125C
- [5SHY 40L4511](#) 1430 A, 125C
- [5SHY 55L4500](#) 1870 A, 125C

**Figure 10 Matching IGCTs for selection**

Up to 4 elements can be selected simultaneously and simulated. If one or more elements produce results exceeding the safe operating area (SOA), no results are returned. In this case, the user should run the simulation again with changed parameters and/or product selection to enable results within SOA operating conditions.

### 3.2.2 Matching Antiparallel Diodes

Once the IGCT is selected then the user can select the matching Antiparallel Diode based on the voltage and current ratings. By clicking on the product code name the user may access the data sheet from the ABB website. This section/figure is not visible when the selected IGCT is RC-IGCT.

AntiParallel Diodes:

- [5SDF 20L4520](#) 4.5KV, 1950 A, 140C
- [5SDF 28L4520](#) 4.5KV, 2620 A, 140C

**Figure 11 Matching Antiparallel Diodes for selection**

## Simulation Settings

### 3.2.3 Matching NPC Diodes

Once the IGCT is selected then the user can select the matching NPC Clamp Diode based on the voltage and current ratings. By clicking on the product code name the user may access the data sheet from the ABB website. This section/figure is not visible when the selected topology is TNPC.

NPC DIODE SELECTION:

[5SDF 20L4520](#) 4.5KV, 1950 A, 140C

[5SDF 28L4520](#) 4.5KV, 2620 A, 140C

**Figure 12 Matching NPC Diodes for selection**

### 3.3 Selection of Articles / Start simulation

To simulate one or more articles, select from the list by activating the checkbox

Simulate	Starts the simulation The progress of the simulation is shown with number of calculated Jacobian.
Abort	Stops the simulation; No results generated
Hold results	To compare multiple simulations, results can be hold for later viewing By selecting the button, result are hold after simulation has finalized for later comparison with succeeding simulations



**Figure 13 Start of simulation**



Calculate Jacobian: 37/55

**Figure 14 Simulation progress and termination**

## 4. SIMULATION RESULTS

The simulation results are displayed in two different ways for all selected articles simulated.

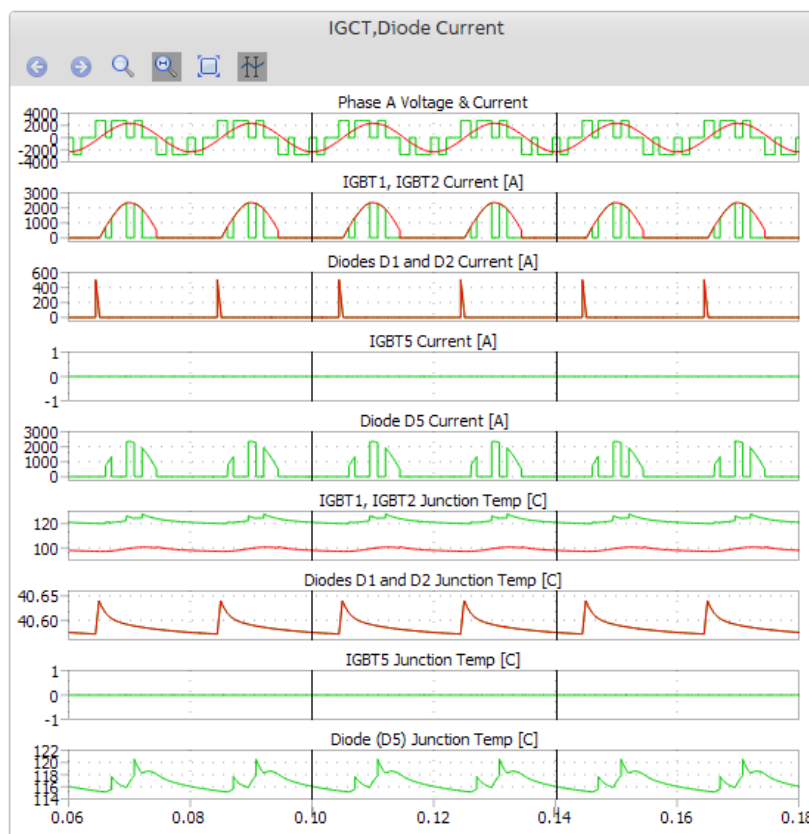
Graphical results - Waveforms Visual analysis of waveforms for fast and efficient detection of most significant sources

Numerical / Tabular results Numeric indication of all simulations values for direct comparison

**Remark:** To hide curves of selected articles, unselect in the table “Results History”

### 4.1 Graphical Output – Waveforms

When the simulation finishes the semiconductor and AC side waveforms are shown as follows:



**Figure 15 Graphical results of Three-phase 2-level VSC converter**

#### 4.1.1 Control

For an indication of values within the graph, a cursor can be activated to show curve values in a table.

Sections of graphs can be zoomed in by click, move and release mouse button for more details

- Hold result Hide selectively waveforms of products
- Rest zoom to full view
- Activate cursors and to show parameter values table according to the cursor position
- Zoom selectable rectangle
- Zoom horizontal or vertical band

## Simulation Results

### 4.1.2 Graph parameters values indication

Tabular indication of graphical wave forms values according cursor position selected. Values are indicated for each parameter Color of wave form is indicated. Third column shows difference of two cursors per parameter.

Name		Cursor 1	Cursor 2	Delta	
Time		0.1	0.14	0.04	🔒
<b>Phase A Voltage &amp; Current</b>					
Phase Voltage-A	🟢	0.000	0.000	0.000	
Phase Current-A	🔴	-2352	-2352	-0.0002375	
<b>IGBT1, IGBT2 Current [A]</b>					
IGCT Currents (a):1	🟢	0.000	0.000	0.000	
IGCT Currents (a):2	🔴	0.000	0.000	0.000	
<b>Diodes D1 and D2 Current [A]</b>					
Diode Currents (a):1	🟢	0.000	0.000	0.000	
Diode Currents (a):2	🔴	0.000	0.000	0.000	
<b>IGBT5 Current [A]</b>					
Switch20	🟢	0.000	0.000	0.000	
<b>Diode D5 Current [A]</b>					
Switch29	🟢	0.000	0.000	0.000	
<b>IGBT1, IGBT2 Junction Temp [C]</b>					
IGCT Junction.Temp (a):1	🟢	121.0	121.0	0.00005200	
IGCT Junction.Temp (a):2	🔴	97.98	97.98	0.00005412	
<b>Diodes D1 and D2 Junction Temp [C]</b>					
Diode Junction Temp (a):1	🟢	40.58	40.58	-0.000002436	
Diode Junction Temp (a):2	🔴	40.58	40.58	-0.000002436	
<b>IGBT5 Junction Temp [C]</b>					
Switch31	🟢	0.000	0.000	0.000	
<b>Diode (D5) Junction Temp [C]</b>					
Switch30	🟢	116.1	116.1	-0.000001008	🔒

**Figure 16** Tabular indication of cursor position graph values

**Remark:**

The numerical values each indicated parameter are shown according the position of the respective cursor in the graph. Drag cursor to investigate about full details

## 4.2 Numerical / Tabular results

The following parameters are given in a tabular format in multiple sections.

The indicated elements in the table upper IGCT etc. correspond to the different semiconductor positions in a full bridge cell as shown in Figure 2 Three-phase 3-level NPC VSC power circuit schematic.

As converter losses the aggregated losses in all 3 phase legs are accounted.

In addition to the semiconductor losses, there are also losses occurring in the passive components (e.g. Resistances, grid-impedances etc.). These Losses are not taken into consideration for this simulation. For the simplicity of the simulation, it is assumed that all semiconductors in one phase leg are loaded symmetrically and no voltage asymmetries do exist.

### Device losses and temperatures

Device Losses & Temperatures						
	Switching	Conduction	Combined Losses	TvjAvg	TvjMax	TvjBLS
IGCT1	1.281 kW	700.40 W	1.982 kW	122.26 °C	127.82 °C	124.5 °C
IGCT2	116.72 W	1.297 kW	1.414 kW	98.69 °C	100.82 °C	100.2 °C
IGCT3	116.72 W	1.297 kW	1.414 kW	98.68 °C	100.82 °C	100.17 °C
IGCT4	1.281 kW	700.35 W	1.981 kW	122.26 °C	127.82 °C	124.55 °C
IGCT5	0 W	0 W	0 W	0 °C	0 °C	0 °C
IGCT6	0 W	0 W	0 W	0 °C	0 °C	0 °C
D1	0 W	15.05 W	15.05 W	40.59 °C	40.64 °C	40.57 °C
D2	0 W	15.05 W	15.05 W	40.59 °C	40.64 °C	40.57 °C
D3	0 W	15.07 W	15.07 W	40.59 °C	40.64 °C	40.59 °C
D4	0 W	15.07 W	15.07 W	40.59 °C	40.64 °C	40.59 °C
D5	847.23 W	1.121 kW	1.968 kW	116.69 °C	120.54 °C	117.4 °C
D6	847.23 W	1.121 kW	1.968 kW	116.69 °C	120.54 °C	117.35 °C
Clamp Loss			2.64 kW			
Converter Losses	13.47 kW	18.89 kW	32.36 kW			
Total Loss			35.00 kW			
% Losses			0.54 %			

**Figure 17 Device Losses & Temperatures**

**Remarks:** Cells with “0” numbers indicate not used semiconductor for selected topology

Switching Loss	Single IGCT or Diode Losses during turn on and turn off events (dynamic)
Conduction loss	Single IGCT or Diode Losses during on state (static)
Combined losses	Sum of single IGCT or Diode switching and conduction loss.
Converter losses	Sum of all IGCT and Diode losses
% Losses	Defined as the (%) ratio of calculated combined converter losses with respect to the converter MVA rating i.e., total apparent power flow. Since the converter is meant for a THREE-PHASE application, the kVA rating would correspond to total three-phase AC Power delivered by the converter.

Junction Temperature Avg

Junction temperature average during the simulation period

Junction Temperature Max

Maximum junction temperature during simulation period

## Simulation Results

### Junction Temperature BLS

Junction temperature at timepoint just before the last switching, after which the maximum junction temperature is achieved

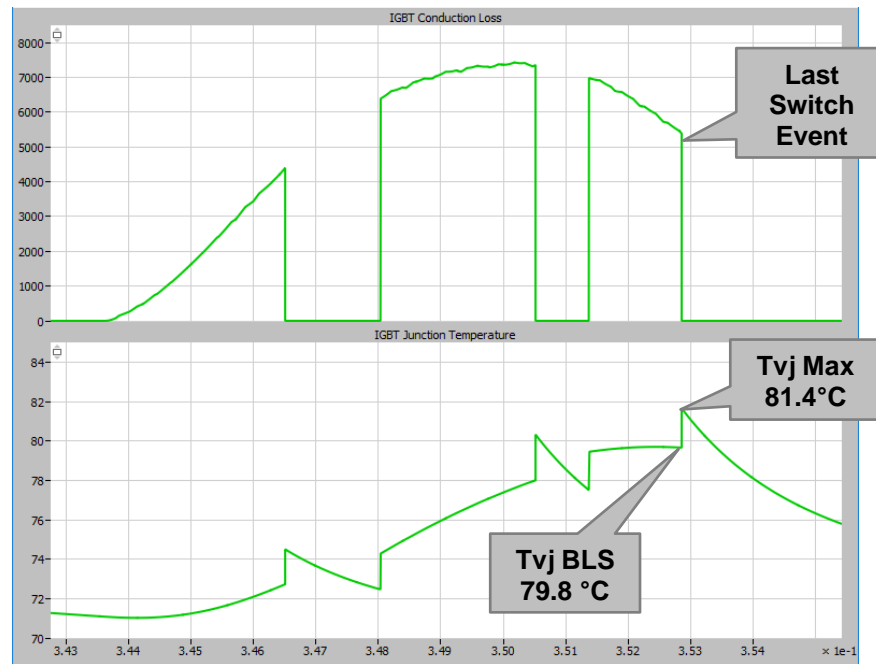


Figure 18 Definition of Tvj before last switch

### Converter AC parameters

Converter AC Parameters						
	Real Power	Reactive Power	Phase Voltage (RMS)	Phase Current (RMS)	Output Frequency (Hz)	Power Factor
	6320 kW	4740 kVAR	1.584 kV	1.663 kA	50 Hz	0.80

Figure 19 Converter AC Parameters

Real power P Active power / real power output of converter

Reactive power Q Q as supplied to grid as effective power (reactive) on converter AC side  
Calculation see in section 8.4.

Phase voltage RMS According AC phase value according 1st order harmonic of AC frequency

Phase current RMS According AC phase value according 1st order harmonic of AC frequency

Output frequency According definition

### DC Parameters & Control Parameters

DC Parameters & Control Parameters				
	DC Power	DC Voltage	Switching Frequency	Modulation Index
	6355 kW	5.600 kV	300 Hz	0.80

Figure 20 Control Parameters

DC Power According AC Power/Current definition + Losses

DC Voltage According definition

Switching Freq. According definition

Modulation Ind. According calculations defined in chapter 8.2



## 5. EFFICIENCY IMPROVEMENTS BY CONTROL STRATEGY

In some applications where higher output voltage and lower switching losses/harmonic distortion factor are required, standard Sinusoidal PWM (SPWM) technique is not capable of meeting these requirements and it is necessary to use other PWM techniques like Third Harmonic Injection PWM (THIPWM) and Space Vector PWM (SVPWM) and Discontinuous PWMs (DPWM). Both THIPWM and SVPWM work on the principle of Zero Sequence Injection and both can operate at a maximum modulation index of 1.15, whereas the maximum modulation index of 1 applies for SPWM technique. Therefore, THIPWM and SVPWM techniques can produce 15% more maximum output AC voltage with lower switching losses for the same input DC voltage when compared to SPWM. But to realize this advantage, a floating neutral system (Delta load or Star Load with no neutral return) is necessary. Floating neutral prevents Zero Sequence currents (which includes DC and Integer multiples of 3rd harmonic Currents) from flowing as they see a high impedance path. Therefore, sinusoidal waveshape in the output voltage and current is retained even with the injection of Zero Sequence components on the input side. Since the maximum modulation is increased to 1.15, the output voltage and power for THIPWM and SVPWM techniques is increased by 15% for the same output AC side reference current.

Find in Figure 21 below the direct comparison of the three control strategies and the influences on the various parameters with the 3 phase 2 level topology. Only adaption is the selection of the PWM and the setting to the maximum value of the modulation index. It is observed from the results, that THIPWM and SVPWM can transmit higher amounts of power and result in lower %losses compared to SPWM, while the junction temperature rise is similar for all three techniques.

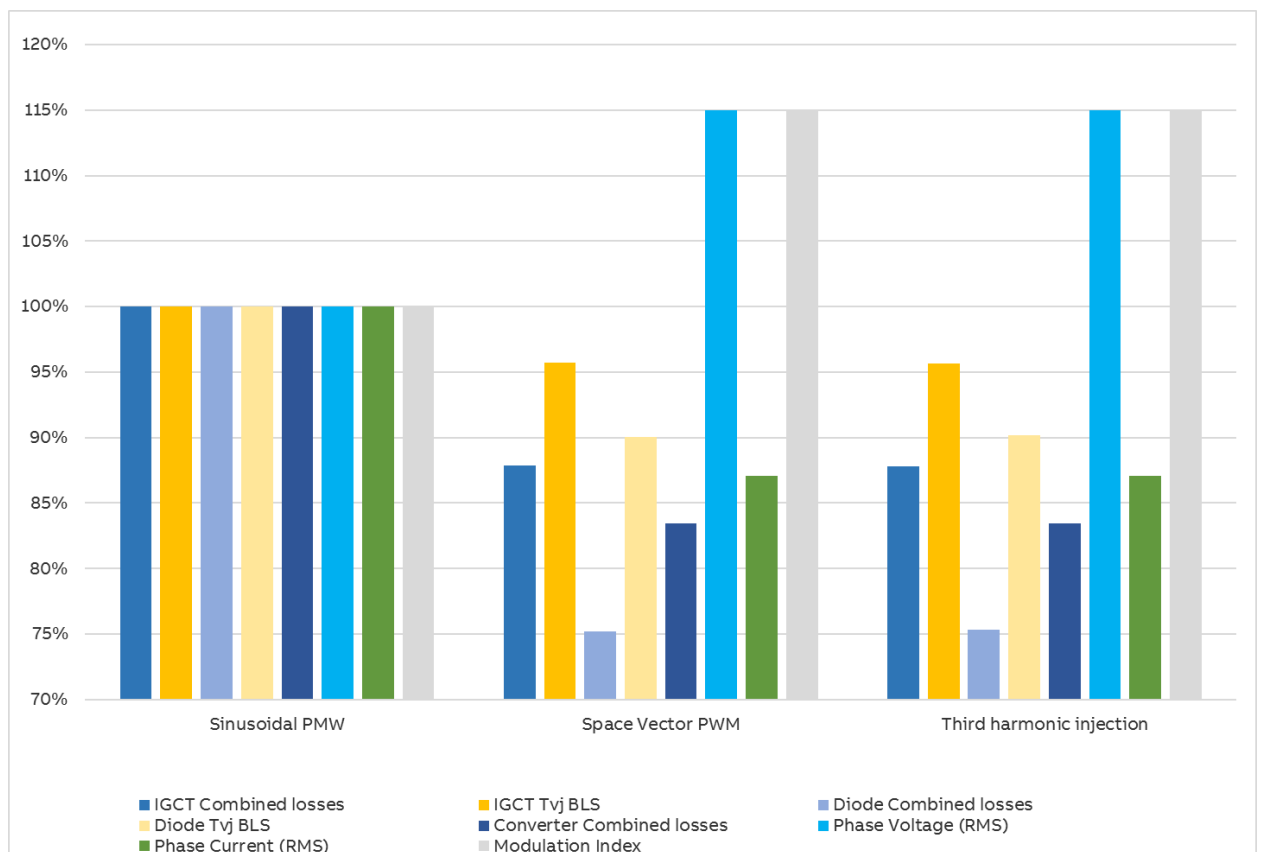


Figure 21 Results diagram comparison of control strategies for NPC

## Efficiency Improvements by Control Strategy

PWM	IGCT Loss [W]	IGCT Tvj [°C]	Diode Loss [W]	Diode Tvj [°C]	Losses [kW]	Losses [%]	Ph Volt [V]	Ph I [A]	Mod Index [1]
Sinusoidal	1568	36.58	829	31.77	7.19	1.01	707	471	1
Space vector	1378	35.01	623	28.61	6.00	0.87	813	410	1.15
3 <sup>rd</sup> harmonic	1376	34.99	625	28.65	6.00	0.87	813	410	1.15

Figure 22 Results table comparison of control strategies for NPC

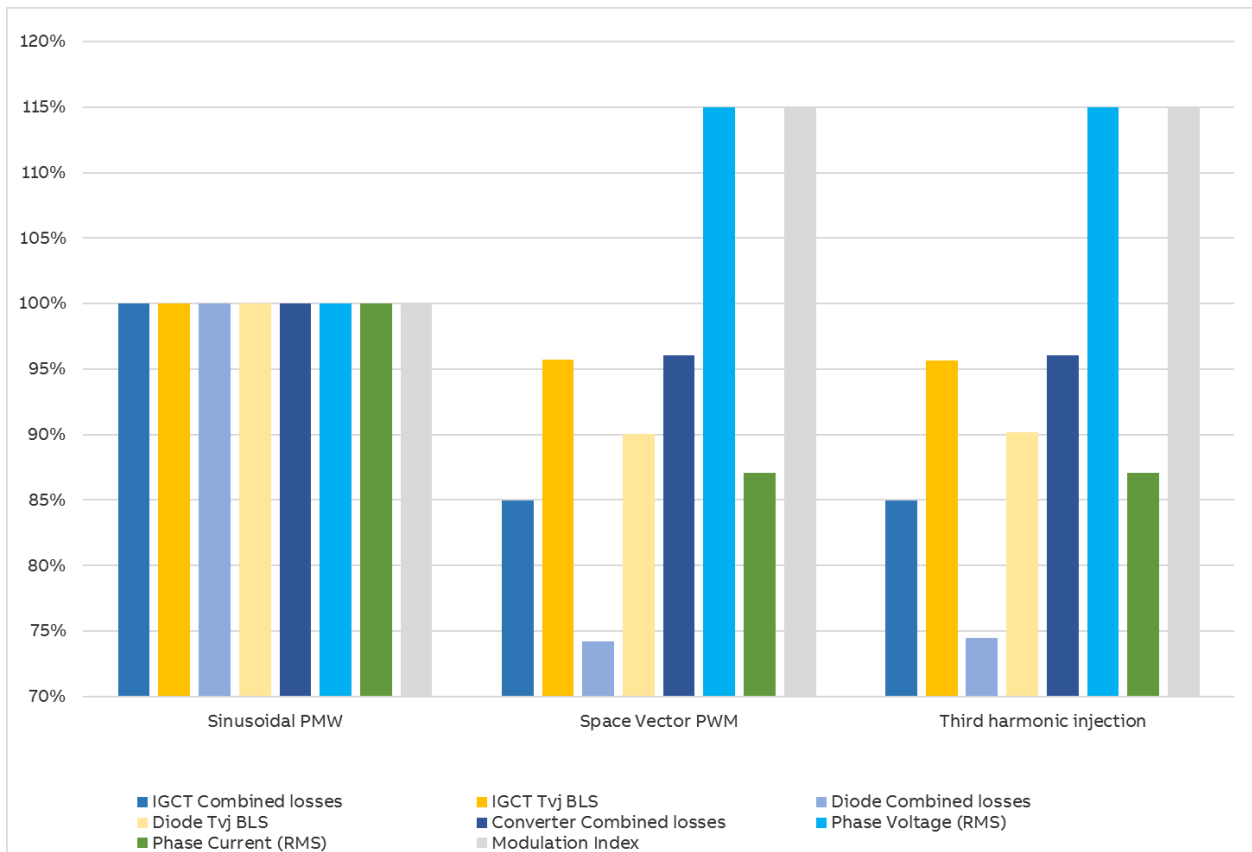


Figure 23 Results diagram comparison of control strategies for TNPC

PWM	IGCT Loss [W]	IGCT Tvj [°C]	Diode Loss [W]	Diode Tvj [°C]	Losses [kW]	Losses [%]	Ph Volt [V]	Ph I [A]	Mod Index [1]
Sinusoidal	1209	36.58	816	31.77	6.07	0.88	707	471	1
Space vector	1027	35.01	605	28.61	5.83	0.73	813	410	1.15
3 <sup>rd</sup> harmonic	1027	34.99	607	28.65	5.83	0.73	813	410	1.15

Figure 24 Results table comparison of control strategies for TNPC

ANPC topology works on standard sinusoidal PWM and three different controls are implemented under sinusoidal PWM namely, DF-PWM, LF/HF PWM and HF/LF PWM. DF-PWM implements high frequency switching over all switches, LF/HF PWM implements low frequency switching over outer switches and high frequency switches over inner switches and HF/LF PWM implements high frequency switching over outer switches and low frequency switches over inner switches.

## **6. SIMULATION CONDITIONS**

All calculations and simulation results are based on datasheet typical values. All types of semiconductor losses are calculated according to PLEXIM PLECS software principle through the reference to the lookup table and linear interpolation of the actual device current, voltage and junction temperature.

## 7. ALERTS & FEATURES

The system verifies results and generated warning messages in case of limits are violated.

### 7.1 Junction Temperature

<b>Parameter</b>	Junction temperature
<b>Verification</b>	If the junction temperature BLS of IGCT and/or diode is above its maximum junction temperature limit, alert message is displayed
<b>Warning message</b>	IGCT temperature out of safe operating area

### 7.2 DC Voltage

<b>Parameter</b>	DC Voltage
<b>Verification</b>	If the DC voltage is greater than safe operating voltage rating of IGCT and/or diode, alert message is displayed
<b>Warning message</b>	For the voltage rating 4.5kV, $V_{dcmin} = 200V$ & $V_{dcmax} = 3400V$

## 8. APPLIED CALCULATIONS

### 8.1 Input Parameter Definitions

PF	User defined load parameter / power factor corresponding to the desired angle between fundamental components of phase voltage and current ( $\cos \varphi_1$ )
V <sub>DC</sub>	Selected DC link voltage
V <sub>Ph_RMS</sub>	Fundamental Phase voltage RMS
I <sub>Ph_RMS</sub>	Fundamental Phase current RMS

### 8.2 Fundamental Phase Voltage RMS of Converter Definition

$$V_{Ph\_RMS} = \frac{m \cdot V_{DC\ Link}}{2\sqrt{2}}$$

### 8.3 Real Power

P <sub>DC</sub>	DC power / real power absorbed from DC side of VSC calculated according
P <sub>AC</sub>	real / active power transferred to converter output calculated as:
V <sub>TrueRMS</sub>	True phase voltage RMS AC
I <sub>TrueRMS</sub>	True phase current RMS AC
η	Power conversion efficiency

$$V_{trueRMS} = \sqrt{\sum_{v=1}^n \widehat{u}_v^2}$$

It includes all harmonic components NOT ONLY 1<sup>st</sup> order of output frequency.

$$I_{trueRMS} = \sqrt{\sum_{v=1}^n \widehat{i}_v^2}$$

It includes all harmonic components NOT ONLY 1<sup>st</sup> order of output frequency.

According to:

$$P_{AC} = 3 * \sum_{v=1}^n \widehat{u}_v \cdot \widehat{i}_v \cdot \cos \varphi_v$$

If current/voltage is free from harmonics, then

$$P_{AC} = 3 * V_{Ph\_RMS} \cdot I_{Ph\_RMS} \cdot PF$$

For Inverter mode, the DC power definition P<sub>DC</sub> can be computed as

$$P_{DC} = P_{AC} + P_{LossConverter}$$

For Rectifier mode, the DC power definition P<sub>DC</sub> can be computed as

$$P_{DC} = P_{AC} - P_{LossConverter}$$

## Applied Calculations

Defined as the Loss (%)  $\eta$  is the ratio of calculated combined converter losses with respect to the converter input power.

For Inverter mode, the  $P_{DC}$  is the main input power definition. Loss (%)  $\eta$  is given by:

$$\eta = \frac{P_{LossConverter}}{P_{DC}} * 100\%$$

For Rectifier mode, the  $P_{AC}$  is the main input power definition. Loss (%)  $\eta$  is given by:

$$\eta = \frac{P_{LossConverter}}{P_{AC}} * 100\%$$

### 8.4 Reactive Power

Q Effective reactive power on converter AC side [VAr]

$$Q = 3 * \sum_{v=1}^n \widehat{u}_v \cdot \widehat{i}_v \cdot \sin \varphi_v$$

If current/voltage is free from harmonics, then

$$Q = 3 * V_{Ph\_RMS} \cdot I_{Ph\_RMS} \cdot \sin \varphi_1$$

## 9. VALIDATION OF SEMIS RESULTS WITH PSCAD

To ensure supplied simulation results are reliable, each SEMIS topology is validated with another simulation system or compared to real measurement data.

The circuit topology is reconstructed in PSCAD to validate the results obtained from the SEMIS web simulation tool. The objective of the work is to develop an open-loop, grid-connected, three-phase two-level VSC simulation model with loss and temperature estimation in PSCAD and to validate the steady-state results obtained through SEMIS-6 web simulation model using sinusoidal pulse-width modulation.

The IGCT and Diode XML data which was created from the device datasheets for SEMIS simulations is modified to individual .txt files for switch turn-on energy ( $E_{on}$ ), switch turn-off energy ( $E_{off}$ ), diode reverse recovery energy ( $E_{rec}$ ), on state voltage drop of IGCT ( $V_t$ ), and on state voltage drop of diode ( $V_d$ ) at different temperatures, to make the data readable in PSCAD.

The PSCAD and SEMIS circuit models are made as identical as possible to prevent any errors in validation due to the dissimilarities. Junction to Case and Case to Heat sink thermal resistances for the IGCT and Diode have been captured from the device datasheet while the Heat sink to ambient thermal resistance  $R_{th(h-a)}$  is assumed as 2K/kW with different ambient temperatures.

Five cases are simulated in PSCAD and SEMIS by varying different parameters like DC Voltage, Switching Frequency, System Frequency, Power Factor, Modulation Index, etc. with the electrical parameters presented in the tables below for comparison. The chosen operating modes cover all the possible combinations of rectifier, inverter, leading power factor, lagging power factor.

It was observed that the difference between the electrical parameters is minimal even after the variations in the operating conditions. It was also observed from the switching, conduction, total converter losses and the device junction temperatures that the results obtained from both SEMIS and PSCAD are very similar and the error percentage is within tolerance (<5%). Therefore, it can be concluded that the results obtained from SEMIS web simulation tool are reliable.

Results analysis according settings															
Topology	SEMIS 5 NPC Three phase three-level VSC with IGBT														
Tester:	Tirthasarathi Lodh, Harshavardhan Marabathina														
Date	2019-11-02														
Instructions	1. Enter all values according the final results table in the column SEMIS 2. Enter all values according the final results from the PSCAD in the column PSCad 3. Verify the relative difference; Results must not vary more than 2%														
Parameter	Set 1 SEMIS	Set 1 PSCad	Set 1 Difference	Set 2 SEMIS	Set 2 PSCad	Set 2 Difference	Set 3 SEMIS	Set 3 PSCad	Set 3 Difference	Set 4 SEMIS	Set 4 PSCad	Set 4 Difference	Set 5 SEMIS	Set 5 PSCad	Set 5 Difference
Average difference [%]			0.59%			0.26%			0.31%			0.52%			0.28%
Max difference [%]			1.96%			3.45%			1.56%			2.37%			1.18%
<b>Device Losses &amp; Temperatures</b>															
Switching Losses IGBT 1 (W)	2994	2969	0.84%	2888	2876	0.42%	1636	1628	0.49%	914	910	0.44%	272	272	0.00%
Switching Losses IGBT 2 (W)	680	673	1.03%	304	303	0.33%	403	404	-0.25%	5120	5086	0.66%	8614	8536	0.91%
Switching Losses Diode 1 (W)	146	146	0.00%	74	74	0.00%	107	107	0.00%	994	974	2.01%	1841	1824	0.92%
Switching Losses Diode 2 (W)	0	0	0.00%	0	0	0.00%	0	0	0.00%	0	0	0.00%	0	0	0.00%
Switching Losses NPC Diode 5 (W)	602	600	0.33%	618	617	0.16%	384	378	1.56%	253	247	2.37%	61	61	0.00%
Conduction Losses IGBT 1 (W)	1310	1293	1.30%	1183	1174	0.76%	904	893	1.22%	141	139	1.42%	6	6	0.00%
Conduction Losses IGBT 2 (W)	2288	2273	0.66%	2115	2110	0.24%	2562	2544	0.70%	1956	1915	2.10%	679	671	1.18%
Conduction Losses Diode 1 (W)	58	57	1.72%	29	28	3.45%	121	120	0.83%	1527	1515	0.79%	1212	1201	0.91%
Conduction Losses Diode 2 (W)	58	57	1.72%	28	28	0.00%	121	121	0.00%	1532	1525	0.46%	1210	1206	0.33%
Conduction Losses NPC Diode 5 (W)	940	931	0.96%	918	915	0.33%	1501	1491	0.67%	1465	1459	0.41%	604	604	0.00%
Combined Losses IGBT 1 (W)	4304	4262	0.98%	4071	4049	0.54%	2540	2520	0.79%	1055	1049	0.57%	279	278	0.36%
Combined Losses IGBT 2 (W)	2968	2946	0.74%	2419	2413	0.25%	2965	2947	0.61%	7076	7002	1.05%	9292	9206	0.93%
Combined Losses Diode 1 (W)	204	203	0.49%	102	102	0.00%	228	227	0.44%	2521	2489	1.27%	3053	3025	0.92%
Combined Losses Diode 2 (W)	58	57	1.72%	28	28	0.00%	121	121	0.00%	1532	1525	0.46%	1210	1206	0.33%
Combined Losses NPC Diode 5 (W)	1541	1531	0.65%	1537	1532	0.33%	1885	1869	0.85%	1718	1706	0.70%	665	665	0.00%
Junction Temperature Avg IGBT 1 (°C)	66	66	0.00%	64	64	0.00%	55	55	0.00%	36	36	0.00%	33	33	0.00%
Junction Temperature Avg IGBT 2 (°C)	58	57	1.72%	54	54	0.00%	58	58	0.00%	70	70	0.00%	82	82	0.00%
Junction Temperature Avg Diode 1 (°C)	51	50	1.96%	49	49	0.00%	47	47	0.00%	51	51	0.00%	55	55	0.00%
Junction Temperature Avg Diode 2 (°C)	46	46	0.00%	45	45	0.00%	47	47	0.00%	54	54	0.00%	55	55	0.00%
Junction Temperature Avg NPC Diode 5 (°C)	55	55	0.00%	55	55	0.00%	58	58	0.00%	41	41	0.00%	31	31	0.00%
Converter Losses (W)	54452	53994	0.84%	48944	48749	0.40%	46434	46103	0.71%	83413	82620	0.95%	86994	86281	0.82%
Losses Efficiency	1.34	1.34	0.00%	0.77	0.76	1.30%	3.9	3.88	0.51%	1.38	1.37	0.72%	0.98	0.97	1.02%
<b>AC Parameters</b>															
Real Power (kW)	3994	3984	0.25%	6329	6329	0.00%	1143.6	1143	0.05%	-6024	-6018	0.10%	-8922	-8910	0.13%
Reactive Power (kVAR)	3995	3974	0.53%	4746	4747	-0.02%	-1929	-1929	0.00%	-8032	-8027	0.06%	4321	4321	0.00%
Phase Voltage RMS (V)	954	954.5	-0.05%	1414	1416	-0.14%	346	344.5	0.43%	1273	1274.5	-0.12%	1909	1909	0.00%
Phase Current RMS (V)	1973	1968	0.25%	1865	1866	-0.05%	2158	2153.5	0.21%	2630	2625	0.19%	1731	1729.5	0.09%
Output Frequency (Hz)	50	50	0.00%	25	25	0.00%	50	50	0.00%	40	40	0.00%	50	50	0.00%
Power Factor	0.707	0.708	-0.14%	0.8	0.8	0.00%	-0.5	-0.5	0.00%	-0.6	-0.6	0.00%	0.9	0.9	0.00%
<b>DC Parameters &amp; Control Parameters</b>															
DC Power (kW)	4049	4038	0.27%	6378	6378	0.00%	1190	1189	0.08%	-5940	-5935	0.08%	-8835	-8835	0.00%
DC Voltage (V)	3000	3000	0.00%	5000	5000	0.00%	1400	1400	0.00%	4000	4000	0.00%	6000	6000	0.00%
Switching Frequency (Hz)	900	900	0.00%	500	500	0.00%	1000	1000	0.00%	800	800	0.00%	1200	1200	0.00%
Modulation Index	0.9	0.9	0.00%	0.8	0.8	0.00%	0.7	0.7	0.00%	0.9	0.9	0.00%	0.9	0.9	0.00%

Figure 25 Validation SEMIS / PSCAD results comparison 3 level 3 phase NPC

## Validation of SEMIS Results with PSCAD

Results analysis according settings															
Topology		SEMIS 5 TNPC Three phase three-level VSC with IGBT													
Tester:		Tirthasarathi Lodh, Harshavardhan Marabathina													
Date		2019-11-02													
Instructions		1. Enter all values according the final results table in the column SEMIS 2. Enter all values according the final results from the PSCAD in the column PSCad 3. Verify the relative difference; Results must not vary more than 2%													
Parameter	Set 1 SEMIS	Set 1 PSCad	Set 1 Difference	Set 2 SEMIS	Set 2 PSCad	Set 2 Difference	Set 3 SEMIS	Set 3 PSCad	Set 3 Difference	Set 4 SEMIS	Set 4 PSCad	Set 4 Difference	Set 5 SEMIS	Set 5 PSCad	Set 5 Difference
Average difference [%]			0.25%			0.35%			0.32%			0.50%			0.31%
Max difference [%]			1.72%			3.45%			1.33%			2.22%			1.29%
<b>Device Losses &amp; Temperatures</b>															
Switching Losses IGBT 1 (W)	2994	2980	0.47%	2888	2876	0.42%	1636	1628	0.49%	914	909	0.55%	272	271	0.37%
Switching Losses IGBT 2 (W)	669	666	0.45%	300	299	0.33%	403	402	0.25%	5116	5077	0.76%	8592	8511	0.94%
Switching Losses Diode 1 (W)	146	146	0.00%	74	74	0.00%	107	107	0.00%	994	978	1.61%	1843	1826	0.92%
Switching Losses Diode 2 (W)	614	613	0.00%	629	627	0.00%	393	387	0.00%	277	271	0.00%	73	73	0.00%
Conduction Losses IGBT 1 (W)	1310	1299	0.84%	1183	1174	0.76%	904	892	1.33%	141	139	1.42%	6	6	0.00%
Conduction Losses IGBT 2 (W)	997	996	0.10%	954	954	0.00%	1646	1635	0.67%	1801	1761	2.22%	670	662	1.19%
Conduction Losses Diode 1 (W)	58	57	1.72%	29	28	3.45%	121	120	0.83%	1527	1513	0.92%	1211	1202	0.74%
Conduction Losses Diode 2 (W)	944	938	0.64%	921	918	0.33%	1510	1499	0.73%	1496	1488	0.53%	616	615	0.16%
Combined Losses IGBT 1 (W)	4304	4279	0.58%	4071	4050	0.52%	2540	2520	0.79%	1055	1048	0.66%	279	277	0.72%
Combined Losses IGBT 2 (W)	1666	1662	0.24%	1255	1253	0.16%	2049	2037	0.59%	6917	6838	1.14%	9292	9172	1.29%
Combined Losses Diode 1 (W)	204	203	0.49%	102	102	0.00%	228	227	0.44%	2521	2491	1.19%	3054	3028	0.85%
Combined Losses Diode 2 (W)	1557	1552	0.32%	1550	1545	0.32%	1902	1886	0.84%	1774	1759	0.85%	689	689	0.00%
Junction Temperature Avg IGBT 1 (°C)	66	66	0.00%	64	64	0.00%	55	55	0.00%	36	36	0.00%	33	33	0.00%
Junction Temperature Avg IGBT 2 (°C)	53	53	0.00%	51	50	1.96%	56	56	0.00%	69	69	0.00%	81	81	0.00%
Junction Temperature Avg Diode 1 (°C)	51	51	0.00%	49	49	0.00%	47	47	0.00%	51	51	0.00%	55	55	0.00%
Junction Temperature Avg Diode 2 (°C)	58	58	0.00%	57	57	0.00%	62	62	0.00%	56	56	0.00%	50	50	0.00%
Converter Losses (W)	46388	46174	0.46%	41866	41700	0.40%	40312	40013	0.74%	73600	72817	1.06%	79706	78998	0.89%
Losses Efficiency	1.15	1.14	0.87%	0.66	0.65	1.52%	3.4	3.38	0.59%	1.22	1.21	0.82%	0.89	0.89	0.00%
<b>AC Parameters</b>															
Real Power (kW)	3994	3996	-0.05%	6329	6329	0.00%	1144	1143	0.09%	-6024	-6018	0.10%	-8927	-8910	0.19%
Reactive Power (kVAR)	3995	3988	0.18%	4746	4747	-0.02%	-1929	-1929	0.00%	-8032	-8027	0.06%	4323.5	4321	0.06%
Phase Voltage RMS (V)	954	956	-0.21%	1414	1416	-0.14%	346	344.5	0.43%	1273	1274.5	-0.12%	1909	1909	0.00%
Phase Current RMS (A)	1973	1973	0.00%	1865	1866	-0.05%	2158	2153.5	0.21%	2630	2625	0.19%	1732	1729.5	0.14%
Output Frequency (Hz)	50	50	0.00%	25	25	0.00%	50	50	0.00%	40	40	0.00%	50	50	0.00%
Power Factor	0.707	0.708	-0.14%	0.8	0.8	0.00%	-0.5	-0.5	0.00%	-0.6	-0.6	0.00%	0.9	0.9	0.00%
<b>DC Parameters &amp; Control Parameters</b>															
DC Power (kW)	4040.5	4043	-0.06%	6371	6371	0.00%	1184	1183	0.08%	-5950	-5945	0.08%	-8847	-8831	0.18%
DC Voltage (V)	3000	3000	0.00%	5000	5000	0.00%	1400	1400	0.00%	4000	4000	0.00%	6000	6000	0.00%
Switching Frequency (Hz)	900	900	0.00%	500	500	0.00%	1000	1000	0.00%	800	800	0.00%	1200	1200	0.00%
Modulation Index	0.9	0.9	0.00%	0.8	0.8	0.00%	0.7	0.7	0.00%	0.9	0.9	0.00%	0.9	0.9	0.00%

Figure 26 Validation SEMIS / PSCAD results comparison 3 level 3 phase TNPC

Validation of ANPC topology is done with IGBT based 3 level 3 phase converter and hence only difference here is replacing IGBT with IGCT.



## 10. REVISION HISTORY

Rev.	Page	Change Description	Date / Initial
1.4	17, 18	Power Definitions	2021-12-06 PGGI/HM
1.3	all	DC Voltage definition	2020-03-04 PGGI/HM
1.2	all	Initial version in new design	2019-08-27 PGGI/DS
1.1	All	Initial version	2018-10-10 PGGI/FMa

## 11. SIMULATION SOFTWARE RELEASE HISTORY

Rev.	New topic	Fixed defects	Tvj influence	Date
1.4	THD of voltage & current measured	-	No	2021-12-03 PGGI/HM
1.3	Improvement of loss balance in phases	Improvement of loss balance in phases	Minor	2021-12-03 PGGI/HM
1.2	Averaging time period is equal to gcd of system and switching frequencies	-	No	2020-06-29 PGGI/HM
1.1	DC Voltage to 2 DC sources	-	No	2020-03-04 PGGI / HM

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