SEMIS Simulation Tool
Single Phase 2-level VSC with IGBT
User manual

INTRODUCTION

SEMIS is a web-based semiconductor simulation tool providing a thermal calculation of the semiconductor losses for common converter circuits. The simulation simplifies significantly the selection of the switching device and enables the optimal selection of semiconductors for further investigations.

The SEMIS Simulation Tool is a user-friendly online application found on ABB Semiconductors website www.abb.com/semiconductors/semis

SEMIS users select from a substantial selection of topologies. By assigning the circuit parameters and selecting the desired switching device, multiple ABB products can be simulated at the same time. Once a simulation is run, SEMIS returns comprehensive results on semiconductor losses as well as on the electrical parameters in the input and output of the circuit. The results are shown in both graphical (waveforms) and numerical (tables) way.

The SEMIS tool is based on the PLECS simulation software. PLECS users can download our product models in the XML file format from the ABB Semiconductors website and use them for their own simulations. For more specific topologies ABB offers customized converter simulations for non-standard topologies with PLECS simulation software on a project basis.

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Release: December 2021

Document number: 5SYA 2120

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1. 1 PHASE 2 LEVEL VSC CONVERTER

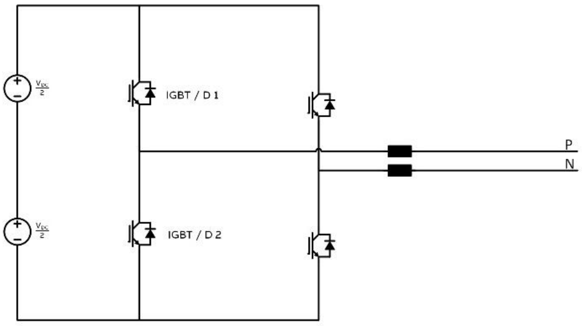
The use of powerful modular single-phase 2-level VSC converters are very popular and have been used in various grid-tied applications for DC-AC (Inverter) and AC-DC (Rectifier) operation. Both
Rectifier and Inverter operations are very common and this has resulted in the use of new Single-phase 2-level VSC widely in various products, due to the simplicity of its power and control architecture.

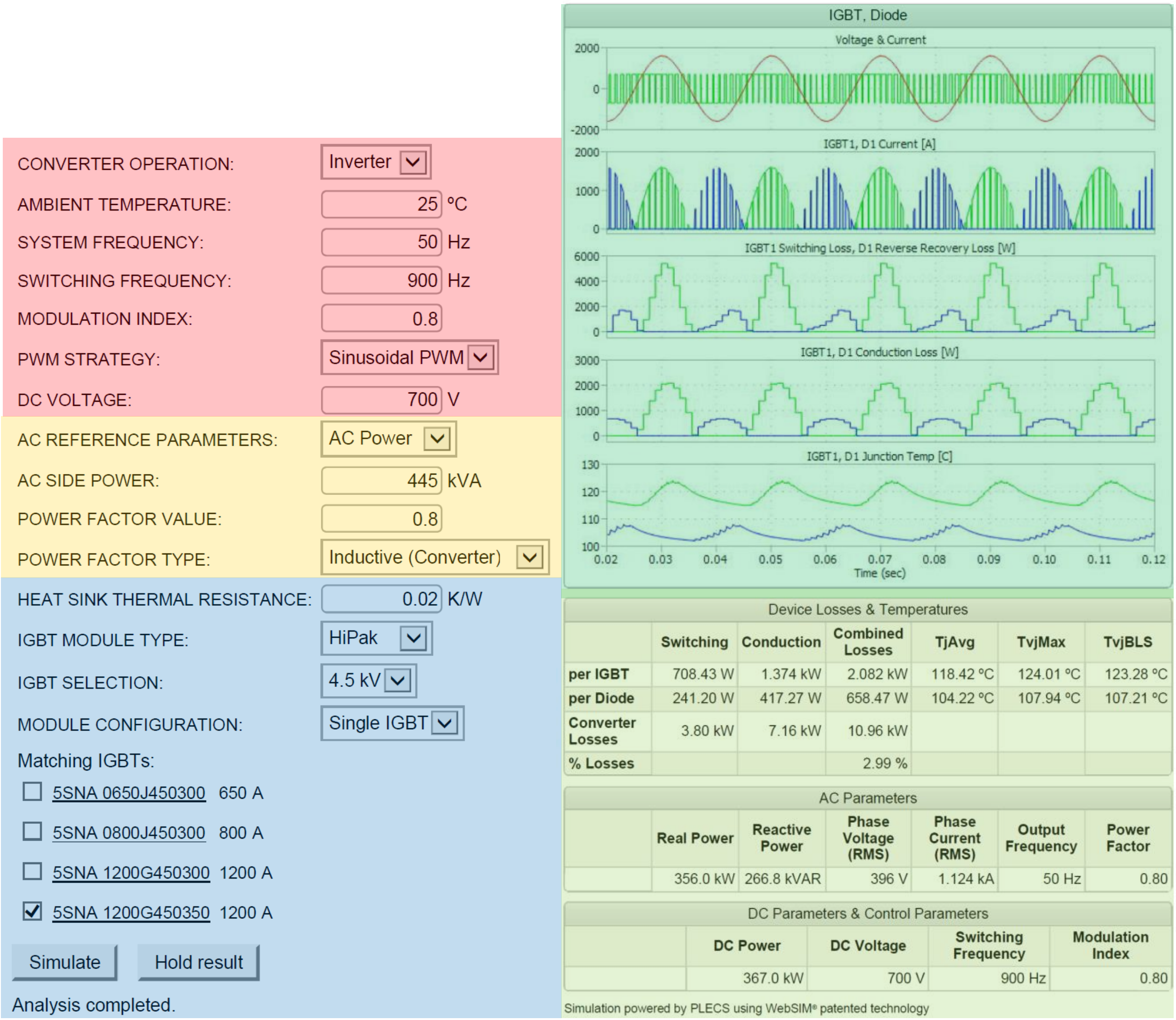
The Single-phase 2-level VSC simplifies equipment design, improves response time and reduces losses.

ABB offers the following Single-phase topologies for thermal analysis simulation with

- Single-Phase Two-level VSC with IGBT
- Single-Phase Three-level VSC with IGBT

2. OVERVIEW





Device Losses & Temperatures

	Switching	Conduction	Combined Losses	TjAvg	TvjMax	TvjBLS
per IGBT	708.43 W	1.374 kW	2.082 kW	118.42 °C	124.01 °C	123.28 °C
per Diode	241.20 W	417.27 W	658.47 W	104.22 °C	107.94 °C	107.21 °C
Converter Losses	3.80 kW	7.16 kW	10.96 kW			
% Losses			2.99 %			

AC Parameters

	Real Power	Reactive Power	Phase Voltage (RMS)	Phase Current (RMS)	Output Frequency	Power Factor
	356.0 kW	266.8 kVAR	396 V	1.124 kA	50 Hz	0.80

DC Parameters & Control Parameters

	DC Power	DC Voltage	Switching Frequency	Modulation Index
	367.0 kW	700 V	900 Hz	0.80

Figure 1 Single-Phase 2-Level converter circuit in website

Grid definitions

Converter settings

IGBT selection

Results graphs

Results tables

3. SIMULATION SETTINGS

3.1 Circuit parameters

3.1.1 Converter Operation

Converter Operation	The converter can be operated either as Inverter DC to AC or as Rectifier AC to DC	Selection

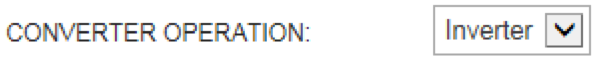


Figure 2 Converter mode selection

3.1.2 Ambient temperature

Ambient temperature	Definition of environmental temperature around the converter for temperature / cooling calculations	Range -25 .. 90 °C

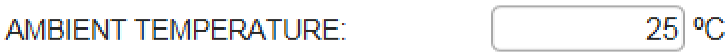


Figure 3 Ambient temperature input block

3.1.3 Controller

The user can define the following parameters as seen in figure 5. The controller generates the switching pulses for the upper and lower IGBTs of the converter.

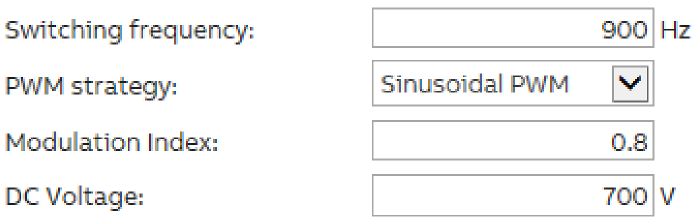


Figure 4 Controller input block

FREQUENCY	Converter AC output frequency	Range 1 to 300 Hz
SWITCHING FREQUENCY	Definition of switching frequency applied for PWM control (Phase-shifted PWM)	Range 100 to 10000 Hz
MODULATION INDEX	Definition of modulation index Over modulation is not possible	Range 0 .. 1
PWM Strategy	Definition of PWM strategy Unipolar PWM and Bipolar PWMs	Selection
DC Voltage	Converter DC Pole-Pole Voltage	Range 100 to 4500 V

3.1.4 Load parameters

The user can enter the desired reference converter AC side current (RMS) or AC power. Further, the user can provide the AC parameters such as power factor and the nature of reactive power to be supplied (Inductive or Capacitive).

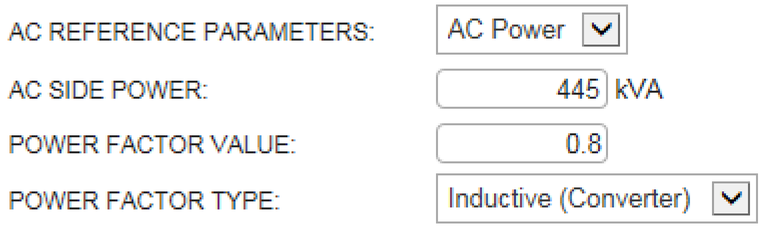


Figure 5 Grid/Load parameter input blocks

AC REFERENCE PARAMETERS	Load Reference can be selected as AC Power when AC Power is the reference AC Current when AC Current (RMS) is the reference	Selection
AC SIDE POWER	AC Side Power demand from the load / connected grid	Range 0.5 ... 5000 kVA
AC SIDE CURRENT(RMS)	AC Side Current demand from the load/ connected grid	Range 1 ... 4000 A
POWER FACTOR VALUE	Power Factor of the load/ connected grid	Range 0 ... 1
POWER FACTOR TYPE	The power factor type can be selected as Inductive or Capacitive based on lagging or leading power factor	Selection

3.2 Switch settings

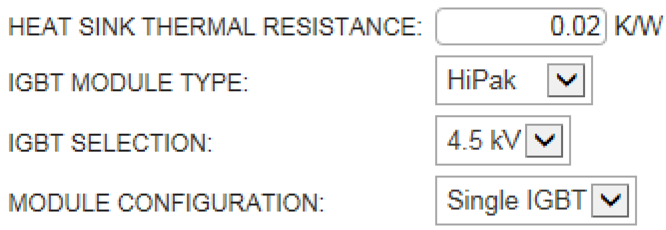


Figure 6 Thermal settings and IGBT selection

Heat Sink Thermal Resistance	Definition of thermal resistance of the cooling system applied.	Range 0.0001 .. 0.5 K/W
Remark:	The value entered is attributed to each individual switch is shown in the electrical configuration schematic of the IGBT module datasheet. Therefore, if a user selects a dual switch module, the Rth should be multiplied with a factor of 2 to differentiate from the single switch case, if the same heatsink would be used in both cases. Same applies for the case of full-bridge modules.	

	The selected Rth is also accounted for the antiparallel diode position for which same consideration applies for its electrical configuration.	
IGBT module type	Select housing type of IGBT for filtering	Selection
IGBT selection	Select voltage class of IGBT for filtering	Selection
Module configuration	Select topology of IGBT module for filtering	Selection

3.2.1 Matching IGBTs

Once the previous IGBT properties are selected, the matching IGBT options appear. By clicking on the product code name the user may access the datasheet from the ABB website.

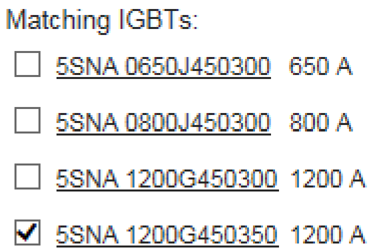


Figure 7 Matching IGBTs for selection

Up to 4 elements can be selected simultaneously and simulated. If one or more elements produce results exceeding the safe operating area (SOA), no results are returned. In this case, the user should run the simulation again with changed parameters and/or product selection to enable results within SOA operating conditions.

3.3 Selection of Articles / Start simulation

To simulate one or more articles, select from the list by activating the checkbox

Simulate	Starts the simulation The progress of the simulation is shown with the number of calculated Jacobian.
Abort	Stops the simulation; No results generated
Hold results	To compare multiple simulations, results can be held for later viewing By selecting the button, result are hold after the simulation has finalized for later comparison with succeeding simulations

Figure 8 Start of simulation

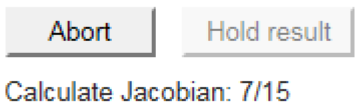


Figure 9 Simulation progress and termination

4. SIMULATION RESULTS

The simulation results are displayed in two different ways for all selected articles simulated.

Graphical results - Waveforms	Visual analysis of waveforms for fast and efficient detection of most significant sources
Numerical / Tabular results	Numeric indication of all simulations values for direct comparison

Remark: To hide curves of selected articles, unselect in the table “Results History”

4.1 Graphical Output – Waveforms

When the simulation finishes the semiconductor and AC side waveforms are shown as follows:

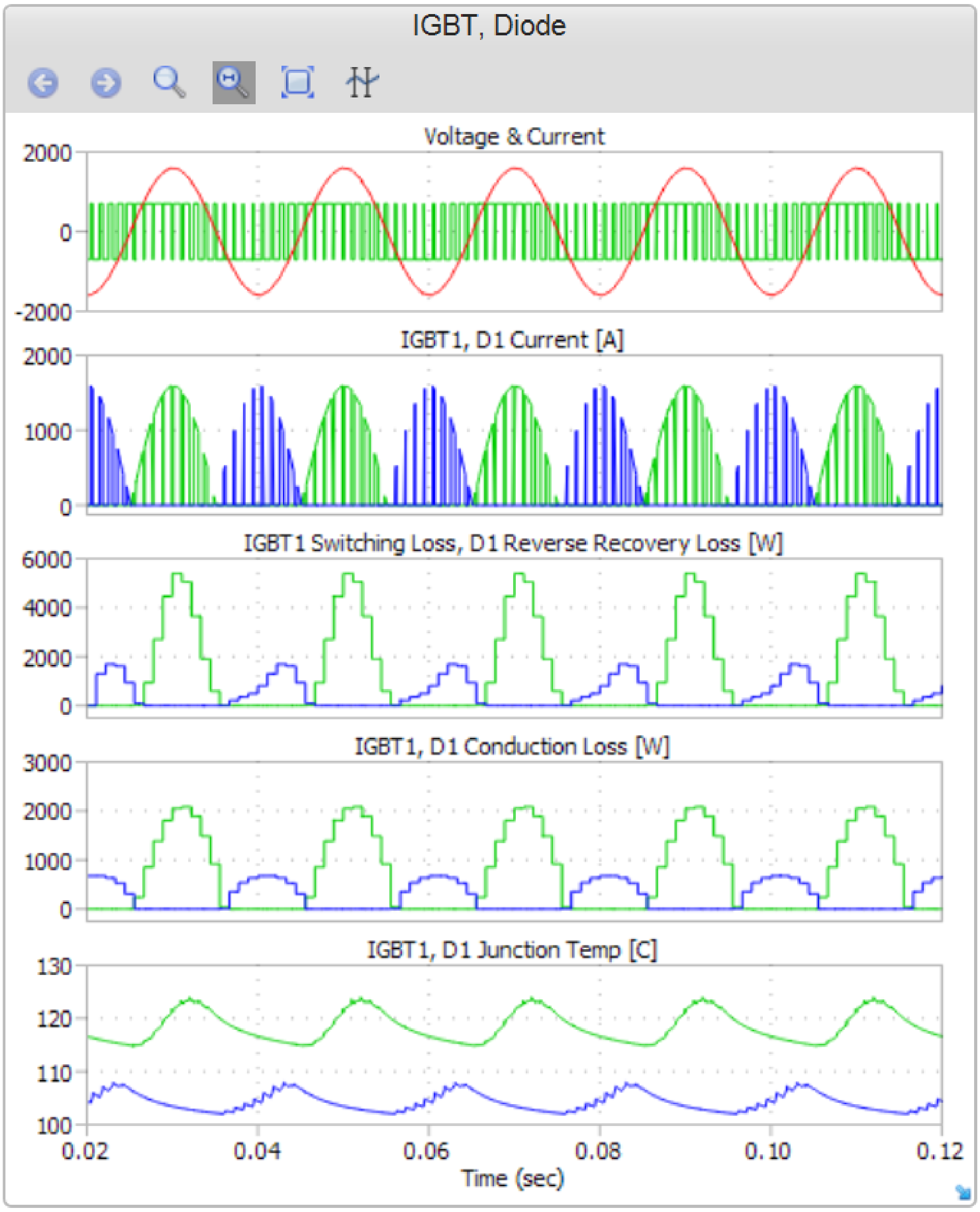


Figure 10 Graphical results of Single-phase 2-level VSC converter

4.1.1 Control

For an indication of values within the graph, a cursor can be activated to show curve values in a table.

Sections of graphs can be zoomed in by click, move and release mouse button for more details

Hold result — Hide selectively waveforms of products

Rest zoom to full view

Activate cursors and to show parameter values table according to the cursor position

Zoom selectable rectangle

Zoom horizontal or vertical band

4.1.2 Parameters values indication

Tabular indication of graphical waveforms values according to cursor position selected.
Values are indicated for each parameter. Color of the waveform is indicated for easier distinction. The third column shows the difference between the two cursors per parameter.

Name	Cursor 1	Cursor 2	Delta
Time	0.053333	0.086667	0.033333
Voltage & Current			
Phase Voltage-A	-700.0	-700.0	0.000
Phase Current-A	840.9	748.0	92.84
IGBT1, D1 Current [A]			
IGBT current	0.000	0.000	0.000
Diode current	0.000	0.000	0.000
IGBT1 Switching Loss, D1 Reverse Recovery Loss [W]			
IGBT Switching Loss	1905	933.8	970.9
Diode Reverse Recovery Loss	0.000	0.5301	-0.5301
IGBT1, D1 Conduction Loss [W]			
IGBT Conduction Loss	1488	239.4	1248
Diode Conduction Loss	0.000	0.000	0.000
IGBT1, D1 Junction Temp [C]			
IGBT Junction Temperature	122.9	115.6	7.299
Diode Junction Temperature	102.5	105.2	-2.684

Figure 11 Tabular indication of cursor position graph values

Remark:
The numerical values each indicated parameter are shown according to the position of the respective cursor in the graph. Drag cursor to investigate about full details

4.2 Numerical / Tabular results

The following parameters are given in a tabular format in multiple sections.

The indicated elements in the table upper IGBT etc. correspond to the different semiconductor positions in a full-bridge cell as shown in **Error! Reference source not found.**.

As converter losses, the aggregated losses in all 3 phase legs are accounted for.

In addition to the semiconductor losses, there are also losses occurring in the passive components (e.g. Resistances, grid-impedances, etc.). These Losses are not taken into consideration for this simulation. For the simplicity of the simulation, it is assumed that all semiconductors in one phase leg are loaded symmetrically and no voltage asymmetries do exist.

Device losses and temperatures

Device Losses & Temperatures						
	Switching	Conduction	Combined Losses	TjAvg	TvjMax	TvjBLS
per IGBT	708.43 W	1.374 kW	2.082 kW	118.42 °C	124.01 °C	123.28 °C
per Diode	241.20 W	417.27 W	658.47 W	104.22 °C	107.94 °C	107.21 °C
Converter Losses	3.80 kW	7.16 kW	10.96 kW			
% Losses			2.99 %			

Figure 12 Device Losses & Temperatures

Switching Loss — Single IGBT or Diode Losses during turn on and turn off events (dynamic)

Conduction loss — Single IGBT or Diode Losses during on state (static)

Combined losses — Sum of single IGBT or Diode switching and conduction loss.

Converter losses — Sum of all IGBT and Diode losses

% Losses — Defined as the (%) ratio of calculated combined converter losses with respect to the converter MVA rating i.e., total apparent power flow. Since the converter is meant for a SINGLE-PHASE application, the kVA rating would correspond to total single-phase AC Power delivered by the converter.

Junction Temperature Avg
: Junction temperature average during the simulation period

Junction Temperature Max
: Maximum junction temperature during the simulation period

Junction Temperature BLS
: Junction temperature at the time point just before the switching, after which the maximum junction temperature is achieved

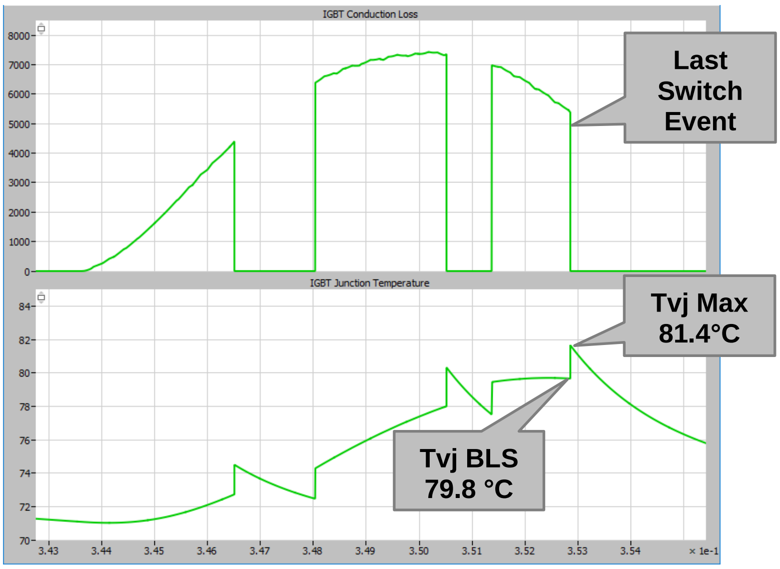


Figure 13 Definition of Tvj before the last switch

Converter AC parameters

Simulation Results

AC Parameters						
	Real Power	Reactive Power	Phase Voltage (RMS)	Phase Current (RMS)	Output Frequency	Power Factor
	356.0 kW	266.8 kVAR	396 V	1.124 kA	50 Hz	0.80

Figure 14 Converter AC Parameters

Real power P Active power / real power output of the converter

Reactive power Q Q as supplied to the grid as effective power (reactive) on converter AC side Calculation see in section 6.4.

Phase voltage RMS According AC phase value according to 1st order harmonics of AC frequency

Phase current RMS According AC phase value according 1st order harmonic of AC frequency

Output frequency According to the definition

DC Parameters & Control Parameters

DC Parameters & Control Parameters				
	DC Power	DC Voltage	Switching Frequency	Modulation Index
	367.0 kW	700 V	900 Hz	0.80

Figure 15 Control Parameters

DC Power According AC Power/Current definition + Losses

DC Voltage According definition

Switching Freq. According to the definition

Modulation Ind. According to calculations defined in chapter 6.2

5. ALERTS & FEATURES

The system verifies results and generated warning messages in case of limits are violated.

5.1 Junction Temperature

Parameter	Junction temperature
Verification	If the junction temperature BLS of IGBT and/or diode is above its maximum junction temperature limit, the alert message is displayed
Warning message	IGBT temperature out of the safe operating area

5.2 DC Voltage

Parameter	DC Voltage
Verification	If the DC voltage is greater than the safe operating voltage rating of IGBT and/or diode, the alert message is displayed
Warning message	For the voltage rating 2.5kV, Vdcmin = 200V & Vdcmax = 1500V

6. APPLIED CALCULATIONS

6.1 Input Parameter Definitions

PF	User defined load parameter / power factor corresponding to the desired angle between fundamental components of phase voltage and current (Cos φ_1)
V_{DC}	Selected DC link voltage
V_{Ph_RMS}	Fundamental Phase voltage RMS
I_{Ph_RMS}	Fundamental Phase current RMS

6.2 Fundamental Phase Voltage RMS of Converter Definition

$$V_{Ph\_RMS} = \frac{m*V_{DC\ Link}}{\sqrt{2}}$$

6.3 Real Power

P_{DC} P_{AC}	DC power / real power absorbed from DC side of VSC calculated according real / active power transferred to converter output calculated as:
$V_{TrueRMS}$	True phase voltage RMS AC
$I_{TrueRMS}$	True phase current RMS AC
η	Power conversion efficiency

$$V_{trueRMS} = \sqrt{\sum_{v=1}^{n} \widehat{u_v^2}}$$

It includes all harmonic components <u>NOT ONLY</u> 1st order of output frequency.

$$I_{trueRMS} = \sqrt{\sum_{v=1}^{n} \widehat{\iota_v^2}}$$

It includes all harmonic components <u>NOT ONLY</u> 1st order of output frequency.

According to:

$$P_{AC} = \sum_{v=1}^{n} \widehat{u_v} \, . \, \widehat{\iota_v} . \cos \varphi_v$$

If current/voltage is free from harmonics, then

$$P_{AC} = V_{Ph\_RMS} . I_{Ph\_RMS} . PF$$

For Inverter mode, the DC power definition P_{DC} can be computed as

$$P_{DC} = P_{AC} + P_{LossConverter}$$

For Rectifier mode, the DC power definition P_{DC} can be computed as

$$P_{DC} = P_{AC} - P_{LossConverter}$$

Defined as the Loss (%) η is the ratio of calculated combined converter losses with respect to the converter input power.
For Inverter mode, the P_{DC} is the main input power definition. Loss (%) η is given by:

$$\eta = \frac{P_{LossConverter}}{P_{DC}} * 100\%$$

For Rectifier mode, the P_{AC} is the main input power definition. Loss (%) η is given by:

$$\eta = \frac{P_{LossConverter}}{P_{AC}} * 100\%$$

6.4 Reactive Power

Q Effective reactive power on converter AC side [VAr]

$$Q = \sum_{\nu=1}^{n} \widehat{u_\nu} \, . \, \widehat{\iota_\nu} . \sin \varphi_\nu$$

If current/voltage is free from harmonics, then

$$Q = V_{Ph\_RMS} . I_{Ph\_RMS} . \sin \varphi_1$$

7. VALIDATION OF SEMIS RESULTS WITH PSCAD

To ensure supplied simulation results are reliable, each SEMIS topology is validated with another simulation system or compared to real measurement data.

The circuit topology is reconstructed in PSCAD to validate the results obtained from the SEMIS web simulation tool. The objective of the work is to develop an open-loop, grid-connected, single-phase three-level VSC simulation model with loss and temperature estimation in PSCAD and to validate the steady-state results obtained through SEMIS-8 web simulation model using sinusoidal pulse-width modulation.

The IGBT and Diode XML data which was created from the device datasheets for SEMIS simulations is modified to individual .txt files for switch turn-on energy (E_{on}), switch turn-off energy (E_{off}), diode reverse recovery energy (E_{rec}), on-state voltage drop of IGBT (V_t), and on state voltage drop of diode (V_d) at different temperatures, to make the data readable in PSCAD.

The PSCAD and SEMIS circuit models are made as identical as possible to prevent any errors in validation due to the dissimilarities. Junction to Case and Case to Heat sink thermal resistances for the IGBT and Diode have been captured from the device datasheet while the Heat sink to ambient thermal resistance $R_{th(h-a)}$ is assumed as 2K/kW with different ambient temperatures.

Five cases are simulated in PSCAD and SEMIS by varying different parameters like DC Voltage, Switching Frequency, System Frequency, Power Factor, Modulation Index, etc. with the electrical parameters presented in the tables below for comparison. The chosen operating modes cover all the possible combinations of rectifier, inverter, leading power factor, lagging power factor.

It was observed that the difference between the electrical parameters is minimal even after the variations in the operating conditions. It was also observed from the switching, conduction, total converter losses and the device junction temperatures that the results obtained from both SEMIS and PSCAD are very similar and the error percentage is within tolerance (<5%). Therefore, it can be concluded that the results obtained from the SEMIS web simulation tool are reliable.

Results analysis according settings

Topology	SEMIS 8 Single phase two level VSC with IGBT
Tester:	Tirthasarathi Lodh, Harshavardhan Marabathina
Date	February 6, 2019

Instructions

1. Enter all values according the final results table in the column SEMIS
2. Enter all values according the final results from the PSCAD in the column PSCad
3. Verify the relative difference; Results must not vary more than 2 %

Parameter	Set 1 SEMIS	Set 1 PSCad	Set 1 Difference	Set 2 SEMIS	Set 2 PSCad	Set 2 Difference	Set 3 SEMIS	Set 3 PSCad	Set 3 Difference	Set 4 SEMIS	Set 4 PSCad	Set 4 Difference	Set 5 SEMIS	Set 5 PSCad	Set 5 Difference
Average difference [%]			0.02%			0.16%			0.40%			0.43%			0.37%
Max difference [%]			0.47%			0.76%			1.03%			1.78%			1.15%
Device Losses & Temperatures															
Switching Losses IGBT 1 (W)	3777	3767	0.26%	3268	3251	0.52%	2092	2078	0.67%	6193	6151	0.68%	9097	9010	0.96%
Switching Losses Diode 1 (W)	821	821	0.00%	739	736	0.41%	487	482	1.03%	1372	1352	1.46%	2260	2239	0.93%
Conduction Losses IGBT 1 (W)	1873	1866	0.37%	1717	1704	0.76%	1772	1754	1.02%	1069	1050	1.78%	347	343	1.15%
Conduction Losses Diode 1 (W)	532	532	0.00%	493	492	0.20%	876	870	0.68%	2353	2334	0.81%	1567	1559	0.51%
Combined Losses IGBT 1 (W)	5650	5633	0.30%	4985	4954	0.62%	3863	3831	0.83%	7262	7201	0.84%	9444	9353	0.96%
Combined Losses Diode 1 (W)	1353	1353	0.00%	1232	1229	0.24%	1363	1352	0.81%	3725	3686	1.05%	3827	3798	0.76%
Junction Temperature Avg IGBT 1 (°C)	76	76	0.00%	72	72	0.00%	65.5	65	0.76%	75	75	0.00%	88.5	88	0.56%
Junction Temperature Avg Diode 1 (°C)	64	64	0.00%	62	62	0.00%	61	61	0.00%	75	75	0.00%	80	80	0.00%
Converter Losses (W)	28013	27944	0.25%	24870	24734	0.55%	20908	20734	0.83%	43949	43550	0.91%	53083	52607	0.90%
Losses Efficiency	2.06	2.05	0.47%	1.16	1.16	0.00%	5.2	5.17	0.58%	2.18	2.17	0.46%	1.78	1.77	0.56%
AC Parameters															
Real Power (kW)	1332	1336	-0.30%	2114	2116	-0.09%	381	380	0.26%	-2009	-2006	0.15%	-2976	-2973	0.10%
Reactive Power (kVAR)	1332	1336	-0.30%	1586	1586	0.00%	-643	-642	0.16%	-2678	-2673	0.19%	1441	1444	-0.21%
Phase Voltage RMS (V)	954	955	-0.10%	1414	1414	0.00%	346	346	0.00%	1273	1272	0.08%	1909	1909	0.00%
Phase Current RMS (A)	1973	1978	-0.25%	1869	1868	0.05%	2157	2154	0.14%	2630	2627	0.11%	1731	1730	0.06%
Output Frequency (Hz)	50	50	0.00%	25	25	0.00%	50	50	0.00%	40	40	0.00%	50	50	0.00%
Power Factor	0.707	0.707	0.00%	0.8	0.8	0.00%	-0.5	-0.5	0.00%	-0.6	-0.6	0.00%	0.9	0.9	0.00%
DC Parameters & Control Parameters															
DC Power (kW)	1360	1364	-0.29%	2139	2140	-0.05%	402	401	0.25%	-1965	-1962	0.15%	-2922	-2920	0.07%
DC Voltage (V)	1500	1500	0.00%	2500	2500	0.00%	700	700	0.00%	2000	2000	0.00%	3000	3000	0.00%
Switching Frequency (Hz)	900	900	0.00%	500	500	0.00%	1000	1000	0.00%	800	800	0.00%	1200	1200	0.00%
Modulation Index	0.9	0.9	0.00%	0.8	0.8	0.00%	0.7	0.7	0.00%	0.9	0.9	0.00%	0.9	0.9	0.00%

Figure 16 Validation SEMIS / PSCAD results in comparison 2 level 1 phase

8. USER MANUAL REVISION HISTORY

Rev.	Page	Change Description	Date / Initial
1.4	12, 13	Power Definitions	2021-12-06 PGGI/HM
1.3	all	DC Voltage definition change	2020-03-04 PGGI/HM
1.2	all	Initial version in new design	2019-09-03 PGGI/DS

9. SOFTWARE RELEASE HISTORY

Rev.	New topic	Fixed defects	Tvj influence	Date
1.3	THD of voltage & current measurement Unipolar PWM is added to PWM strategies	-	No	2021-12-06 PGGI/HM
1.2	Averaging time period is equal to gcd of system and switching frequencies	-	No	2020-06-29 PGGI/HM
1.1	DC Voltage to 2 DC sources	-	No	2020-03-04 PGGI / HM

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